

Wednesday , 25 September	
9:45 10:00	Opening Words
10:00 11:00	Marius Bozga. <i>Modeling Heterogeneous Real-time Components in BIP (revisited)</i>
11:00 11:30	Coffe Break
11:30 13:00	Real-time systems. <ul style="list-style-type: none"> ▪ Jagadish Suryadevara, Cristina Seceleanu, Frederic Mallet and Paul Pettersso, <i>Verifying MARTE/CCSL Mode Behaviors using UPPAAL.</i> ▪ Nadia Menad and Philippe Dhaussy, <i>A Transformation Approach for Multiform Time Requirements.</i> ▪ Bogdan Aman and Gabriel Ciobanu, <i>Real-Time Migration Properties of rTimo Verified in UPPAAL .</i>
13:00 14:30	Lunch
14:30 19:30	Excursion
19:30	City Hall Reception

Thursday, 26 September	
10:00 11:00	Wolfram Schulte. <i>Building billions of software artifacts.</i>
11:00 11:30	Coffe Break
11:30 13:00	Verification 1 <ul style="list-style-type: none"> ▪ Marcel Vinicius Medeiros Oliveira, Ivan Soares De Medeiros Júnior and Jim Woodcock, <i>A verified protocol to implement multi-way synchronisation and interleaving in CSP</i> ▪ Radoslaw Klimek, <i>From Extraction of Logical Specifications to Deduction-based Formal Verification of Requirements Models</i> ▪ Marian Borek, Nina Moebius, Kurt Stenzel and Wolfgang Reif, <i>Model Checking of Security-Critical Applications in a Model Driven Approach</i>
13:00 14:30	Lunch
14:30 16:00	Verification 2 <ul style="list-style-type: none"> ▪ Manuel Gesell, Andreas Morgenstern and Klaus Schneider, <i>Lifting Verification Results for Preemption Statements.</i> ▪ Christian Percebois, Martin Strecker and Hanh Nhi, Tran, <i>Rule-level verification of graph transformations for invariants based on edges' transitive closure.</i> ▪ Gijs Vanspauwen and Bart Jacobs, <i>Sound Symbolic Linking in the Presence of Preprocessing.</i>
16:00 16:30	Coffe Break
16:30 18:00	Types and inference <ul style="list-style-type: none"> ▪ Sebastian Krings and Michael Leuschel, <i>Inferring Physical Units in B Models.</i> ▪ Mahdi Sargolzaei, Francesco Santini, Farhad Arbab and Hamideh Afsarmanes, <i>A Tool for Behaviour-based Discovery of Approximately Matching Web Services.</i> ▪ Ornela Dardha, Elena Giachino and Michael Lienhardt, <i>A Type System for Components.</i>
	Gala Dinner

Friday, 27 September	
10:00 11:00	Thomas A. Henzinger . <i>Behavioral Software Metrics</i>
11:00 11:30	Coffe Break
11:30 13:00	Static analysis <ul style="list-style-type: none"> ▪ Sarmen Keshishzadeh, Arjan Mooij and Mohammadreza Mousavi, <i>Early Fault Detection in DSLs using SMT Solving and Automated Debugging</i> ▪ Iman Saleh, Gregory Kulczycki, M.Brian Blake and Yi Wei, <i>Static Detection of Implementation Errors Using Formal Code Specification</i> ▪ Thi Mai Thuong Tran, Martin Steffen and Hoang Truong, <i>Compositional Static Analysis for Implicit Join Synchronization in a Transactional Setting</i>
13:00 14:30	Lunch
14:30 16:00	Testing and runtime verification <ul style="list-style-type: none"> ▪ Maximiliano Cristia, Gianfranco Rossi and Claudia Frydman, <i>{log} as a Test Case Generator for the Test Template Framework</i> ▪ Daniel Wonisch, Alexander Schremmer and Heike Wehrheim, <i>Zero Overhead Runtime Monitoring</i> ▪ Stijn De Gouw, Frank S. De Boer and Peter Y. H. Wong, <i>Run-Time Verification of Coboxes</i>
16:00 16:30	Coffe Break
16:30 18:00	Synthesis and transformation <ul style="list-style-type: none"> ▪ Amel Bennaceur, Chris Chilton, Malte Isberner and Bengt Jonsson, <i>Automated Mediator Synthesis: Combining Behavioural and Ontological Reasoning</i> ▪ Ran Ji, Reiner Hähnle and Richard Bubel, <i>Program Transformation Based on Symbolic Execution and Deduction</i> ▪ Yunja Choi, <i>Constraint Specification and Test Generation for OSEK/VDX-based Operating Systems</i>