

Testing Semantics for RPTA

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Abstract. The language RPTA, Real Time Process Algebra, has been created to enable rigorous treatment of knowledge representation and manipulation in terms of to be I to have / to do in a formal and coherent framework. This language has been designed to cope with the three dimensions involved in the problem of software specification: (i) mathematical operations, (ii) event/process timing, and (iii) memory manipulation. In this paper we focus on giving a testing semantics to the second dimension: Process timing dimension. First, we will provide a SOS like operational semantics for the process relations of RPTA. Next, we will define what a test is and we will introduce a relation based on which tests are passed by processes. Finally, we will obtain an operational characterization that can be used as a first step to define a denotational semantics sound and complete with respect the testing semantics.

Keywords: RPTA, real time process algebra, testing semantics

1. Introduction

During the last years, *cognitive informatics* [36, 38, 44, 42] is consolidating its position among more mature research schools. In fact, this area is emerging as a new, separate, interdisciplinary branch of

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No.	Meta-process	Syntax	Operational Semantics
1.1	System	$\S(SysID_S)$	Represents a system, $SysID$, identified by a string, S
1.2	Assignment	$y_{Type} := x_{Type}$	if $x.type = y.type$ then $x.value \Rightarrow y.value$ else !(@AssignmentTypeError _S) where $Type \in Meta - Types$
1.3	Addressing	$ptrP^{\wedge} := x_{Type}$	if $prt.type = x.type$ then $x.value \Rightarrow ptr.value$ else !(@AssignmentTypeError _S) where $Type \in \{H, Z, P^{\wedge}\}$
1.4	Input	$Port(ptrP^{\wedge})_{Type} > x_{Type}$	if $Port(ptrP^{\wedge}).type = x.type$ then $Port(ptrP^{\wedge}).value \Rightarrow x.value$ else !(@InputTypeError _S) where $Type \in \{B, H\}, P^{\wedge} \in \{H, N, Z\}$
1.5	Output	$x_{Type} < Port(ptrP^{\wedge})_{Type}$	if $Port(ptrP^{\wedge}).type = x.type$ then $x.value \Rightarrow Port(ptrP^{\wedge}).value$ else !(@OutputTypeError _S) where $Type \in \{B, H\}, P^{\wedge} \in \{H, N, Z\}$
1.6	Read	$Mem(ptrP^{\wedge})_{Type} > x_{Type}$	if $Mem(ptrP^{\wedge}).type = x.type$ then $Mem(ptrP^{\wedge}).value \Rightarrow x.value$ else !(@ReadTypeError _S) where $Type \in \{B, H\}, P^{\wedge} \in \{H, N, Z\}$

Table 1. RTPA meta-processes (1/3).

research. Even though there has been a lot of progress, cognitive informatics is still in a preliminary phase. Thus, we still need good formalisms to represent the different actors involved in the behavior of cognitive processes. In order to show that this is a truly interdisciplinary area, researchers in cognitive informatics very often take advantage of developments in other areas. Thus, they often adapt implementation and representation languages, theoretical models, and algorithms as well as consider best practices obtained in these areas. In this line, *descriptive mathematics for cognitive informatics* introduced as a representation language an adaption of classical process algebras [16, 24, 2, 25, 3]. Process algebras are very suitable to formally specify systems where concurrency is an essential key. This is so because they allow to model these systems in a compositional manner. Next, we briefly review the main milestones in the development of process algebras (see [3] for a good overview of the current research topics in the field). The first work on process algebras settled an important theoretical background for the study of concurrent and distributed systems. In fact, this work was very significant, mainly to shed light on concepts and to open research methodologies. However, due to the abstraction of the complicated features, models were still far from real systems. Therefore, some of the solutions were not specific enough, for instance, those related to real time systems. Thus, researchers in process algebras have tried to bridge the gap between formal models described by process algebras and real systems. In particular, features which were abstracted before have been introduced in the models. Thus, they allow the design of systems where not only functional requirements but also performance ones are included. The most significant of these additions are related to notions such as time (e.g. [34, 26, 45, 8, 17, 18, 1]) and probabilities (e.g.

No.	Meta-process	Syntax	Operational Semantics
1.7	Write	$x_{Type} < \text{Mem}(ptrP^{\wedge})_{Type}$	if $\text{Mem}(ptrP^{\wedge}).type = x.type$ then $x.value \Rightarrow \text{Mem}(ptrP^{\wedge}).value$ else $!(@\text{WriteTypeError}_S)$ where $Type \in \{B, H\}, P^{\wedge} \in \{H, N, Z\}$
1.8	Timing	a) $@t_{hh:mm:ss:ms} := \S t_{hh:mm:ss:ms}$ b) $@t_{yy:MM:dd} := \S t_{yy:MM:dd}$ c) $@t_{yy:MM:dd:hh:mm:ss:ms} := \S t_{yy:MM:dd:hh:mm:ss:ms}$	if $@t.type = \S t.type$ then $\S t.value \Rightarrow @t.value$ else $!(@\text{TimingTypeError}_S)$ where $yy \in \{0, \dots, 99\}, MM \in \{1, \dots, 12\},$ $dd \in \{1, \dots, 31\}, hh \in \{0, \dots, 23\},$ $mm, ss \in \{0, \dots, 59\}, ms \in \{0, \dots, 999\}$
1.9	Duration	$@tn_Z := \S tn_Z + \Delta n_Z$	if $@tn.type = \Delta n.type = \S tn.type = Z$ then $(\S tn.value + \Delta n.value) \text{ mod } \text{MaxValue} \Rightarrow @tn.value$ where MaxValue is the upper bound of the system relative-clock, and the unit of all values is ms
1.10	Memory allocation	$\text{AllocateObject}(\text{ObjectID}_S, \text{NofElements}_N, \text{ElementType}_{RT})$	$n_N := \text{NofElements} \rightarrow$ $R_{i=1}^n(\text{new ObjectID}(i_N) : \text{ElementType}_{RT}) \rightarrow$ $\textcircled{S} \text{ObjectID.Existed}_{BL} := \text{true}$
1.11	Memory release	$\text{ReleaseObject}(\text{ObjectID}_S)$	$\text{delete ObjectID}_S //$ $\text{System.Garbage Collection}() \rightarrow$ $\text{ObjectID}_S := \text{null} \rightarrow$ $\textcircled{S} \text{ObjectID.Release}_{BL} := \text{true}$

Table 2. RTPA meta-processes (2/3).

[10, 29, 28, 7, 6, 27]). An attempt to integrate time and probabilistic information has been given by introducing *stochastic process algebras* (e.g. [11, 15, 4, 13, 5, 32, 19, 23]). The idea underlying the definition of stochastic time is that the actual amount of time is given by taking into account different probabilities.

It is worth to note that even though process algebras were not originally created to describe cognitive processes, they are a very suitable mechanism to do it since they are very appropriate to define systems where concurrency plays a fundamental role. In fact, variants of process algebras have been already used several times to represent *human* processes (see, for example, [30, 22, 39, 43, 31]). The first real *cognitive process algebra* is RTPA [37, 40]. conveniently putting together the ideas underlying the definition of classical process algebras, RTPA is a new mathematical framework to represent cognitive processes and systems. As professor Y. Wang states in [41]:

RTPA is a real-time process algebra that can be used to formally and precisely describe and specify architectures and behaviors of human and software systems.

By Following RTPA, the process algebra STOPA [20, 33, 21] represents an advance since the notion of time is further developed to consider the representation of *stochastic time*.

The process algebra literature includes numerous semantic models. These semantic frameworks are used to describe the behavior of processes as well as to define relations on them. Testing semantics [9, 14] represents one of these semantic frameworks. Intuitively, two processes are *testing equivalent* if they

No.	Meta-process	Syntax	Operational Semantics
1.12	Increase	$\uparrow (n_{Type})$	if $n.value < \text{MaxValue}$ then $n.value + 1 \Rightarrow n.value$ else $!(@\text{ValueOutOfRange}_S)$ where $Type \in \{N, Z, B, H, P^{\wedge}\}$ $\text{MaxValue} = \min\{\text{run-time defined upper bound, nature upper bound of } Type\}$
1.13	Decrease	$\downarrow (n_{Type})$	if $n.value > 0$ then $n.value - 1 \Rightarrow n.value$ else $!(@\text{ValueOutOfRange}_S)$ where $Type \in \{N, Z, B, H, P^{\wedge}\}$
1.14	Exception detection	$!(@e_S)$	$\uparrow (\text{ExceptionLogPtr}_{P^{\wedge}}) \rightarrow$ $@e_S \Rightarrow \text{Mem}(\text{ExceptionLogPtr}_{P^{\wedge}})_S$
1.15	Skip	\emptyset	Exit a current control structure, such as loop, branch, or switch
1.16	Stop	stop	System stop

Table 3. RTPA meta-processes (3/3).

have the same *responses* for all the *tests* belonging to a certain set. Depending on how these responses are analyzed, several testing semantics can be defined: may, must, refusal, fair, etc. We consider that this semantic framework is very suitable because it is easy to understand and allows us to give different equivalences just by modifying the idea of what a test is or when a test is successfully passed.

In this paper we adapt the classical testing framework to provide both a must and a may testing semantics for RTPA. The direct definition of these semantics relations is very hard to handle in the sense that we should apply an infinite number of test just to check if two processes are related. So, in addition to the definitions, we will give the corresponding characterizations based on the operational semantics of the processes.

The rest of the paper is structured as follows. In the next section we will recall from [37] the RTPA language. This language is very expressive but too complex to formally study so, in Section 3, we present the core language that we will be the subject of our study. In Section 4 we present the SOS like operational semantics of the core language. Next, in Section 5 we present the definition of the *must* and *may* testing semantics. The characterization of both testing semantics will be given in terms of *states* and *barbs* that will be introduced in Section 6. Then, we present the characterization of the *must* testing semantics in Section 7 and the corresponding characterization of the *may* testing semantics in Section 8. Finally, in Section 9, we present the conclusions of our paper.

2. The RTPA language

An RTPA *process* is a basic unit of software system behaviors that represents a transition procedure of a system from one state to another by changing its sets of inputs, outputs, and/or internal variables.

A process can be defined as a single *meta-process* or as a complex process based on meta-processes using *process relations*. Thus, RTPA is described by using the following structure:

No.	Meta-type	Syntax
2.1	Natural number	N
2.2	Integer	Z
2.3	Real	R
2.4	String	S
2.5	Boolean	$BL = \{\text{true}, \text{false}\}$
2.6	Byte	B
2.7	Hexadecimal	H
2.8	Pointer	P^{\wedge}
2.9	Time	$hh : mm : ss : ms$ where $hh \in \{0, \dots, 23\}, mm, ss \in \{0, \dots, 59\},$ $ms \in \{0, \dots, 999\}$
2.10	Date	$yy : MM : dd$ where $yy \in \{0, \dots, 99\}, MM \in \{1, \dots, 12\},$ $dd \in \{1, \dots, 31\}$
2.11	Date/Time	$yyyy : MM : dd : hh : mm : ss : ms$ where $yyyy \in \{0, \dots, 9999\}, MM \in \{1, \dots, 12\},$ $dd \in \{1, \dots, 31\}, hh \in \{0, \dots, 23\},$ $mm, ss \in \{0, \dots, 59\}, ms \in \{0, \dots, 999\}$
2.12	Run-time determinable type	RT
2.13	System architectural type	ST
2.14	Event	$@e_S$
2.15	Status	$\textcircled{S} s_{BL}$

Table 4. RTPA meta-types.

$RTPA ::=$

- Meta-processes
 - | Primary types
 - | Abstract data types
 - | Process relations
 - | System architectures
 - | Specification refinement sequences

$RTPA$ is a formal method for specifying software system architectures, static and dynamic behaviors. This language distinguishes the concepts of meta-processes from complex processes and complex relations. A meta-process is an elementary process that serves as a basic building block in a software system. Complex processes can be derived from meta-processes according to given process combinatory rules. The syntax and operational semantics of the meta-processes are given in Tables 1, 2 and 3. Each meta-process is a basic operation on one or more operands such as variables, memory elements, or I/O ports. Structures of the operands and their allowable operations are constrained by their types.

The $RTPA$ notation is strongly typed. Every operand in $RTPA$ is assigned with a data type labeled as a suffix. The definition of the primary data types are the meta-types defined from 2.1 to 2.10 in Table 4. The meta-types date/time (2.11) are special types for continuous real-time systems, where long-range timing manipulation is needed. The runtime determinable (2.12) is a subset of all the rest meta-types defined, which is designed to support flexible type specification that is unknown at compile-time, but will be instantiated at run-time. The system architectural components (2.13) is a novel and important

No.	ADT	Syntax	Designed behaviors
3.1	Stack	Stack:ST	Stack. (Create, Push, Pop, Clear, EmptyTest, FullTest, Release)
3.2	Record	Record:ST	Record. (Create, fieldUpdate, Update, FieldRetrieve, Retrieve, Release)
3.3	Array	Array:ST	Array. (Create, Enqueue, Serve, Clear, EmptyTest, FullTest, Release)
3.4	Queue (FIFO)	Queue:ST	Queue. (Create, Enqueue, Serve, Clear, EmptyTest, FullTest, Release)
3.5	Sequence	Sequence:ST	Sequence. (Create, Retrieve, Append, Clear, EmptyTest, FullTest, Release)
3.6	List	List:ST	List. (Create, FindNext, FindPrior, Findith, FindKey, Retrieve, Update, InsetAfter, InsertBefore, Delete, CurrentPos, FullTest, EmptyTest, SizeTest, Clear, Release)
3.7	Set	Set:ST	Set. (Create, Assign, In, Intersection, Union, Difference, Equal, Subset, Release)
3.8	File (Sequential)	SeqFile:ST	SeqFile. (Create, Reset, Read, Append, Clear, EndTest, Release)
3.9	File (Random)	RandFile:ST	RandFile. (Create, Reset, Read, Write, Clear, EndTest, Release)
3.10	Binary Tree	BTree:ST	BTree. (Create, TReverse, Insert, DeleteSub, Update, Retrieve, Find, Characteristics, EmptyTest, Clear, Release)

Table 5. RTPA abstract data types.

data type in RTPA that models system architectural components. The event and status types are used to model systems event variables (2.14) as a string type, and system status variables (2.15) as a Boolean type.

In addition to the meta-types for system modeling, a set of typical and frequently used combinational data objects in system architectural modeling, the abstract data types are selected and predefined in RTPA. They are described in Table 5. The interested reader may find in [37] a detailed explanation of all the definitions appearing in Tables 1-5 as well as the definitions of *system architectures* and *specification refinement sequences*. In Tables 6 and 7 the syntax and operational semantics of the process relations are described.

3. The Core Language $RTPA_{core}$

The RTPA language is a very high level language and that enormously complicates its formal study. Therefore we need to define a core language that we will call $RTPA_{core}$. This language will be slightly simpler than the process relations defined in RTPA, but it will be powerful enough to represent all process relations in RTPA.

In order to describe the $RTPA_{core}$ language, we will first introduce the set of actions *Act*. An action represents a change in the system: A communication of a value, the evaluation of an expression, an event

No.	Process relation	Syntax	Operational semantics
4.1	Sequence	$P \rightarrow Q$	$P \rightarrow Q$
4.2	Branch	$(?expBL = \mathbf{true}) \rightarrow P$ $ (?expBL = \mathbf{false}) \rightarrow Q$	if $expBL = \mathbf{true}$ then P else Q
4.3	Switch	$?expNUM =$ $0 \rightarrow P_0$ $1 \rightarrow P_1$ \dots $n - 1 \rightarrow P_{n-1}$ $else \rightarrow \emptyset$	case $expNUM =$ $0 : P_0$ $1 : P_1$ \dots $n - 1 : P_{n-1}$ $else : exit$
4.4	For-do	$R_{i=1}^n P(i)$	for $i := 1$ to n do $P(i)$
4.5	Repeat	$R_{\geq 1}^{expBL \neq \mathbf{true}} P$	repeat P until $expBL \neq \mathbf{true}$
4.6	While-do	$R_{\geq 0}^{expBL \neq \mathbf{true}} P$	while $expBL = \mathbf{true}$ do P
4.7	Function call	$P_{\hookrightarrow} F$	$P' \rightarrow F \rightarrow P''$ where $P = P' \cup P''$
4.8	Recursion	$P \circlearrowleft P$	$P' \rightarrow P \rightarrow P''$ where $P = P' \cup P''$
4.9	Parallel	$P \parallel Q$	MPSC (multi-processor single clock) internal parallel
4.10	Concurrence	$P \not\& Q$	MPMC (multi-processor multi-clock) external parallel
4.11	Interleave	$P \parallel\parallel Q$	SPSC (single processor single clock) internal virtual parallel
4.12	Pipeline	$P \gg Q$	$P \rightarrow Q$ and $\{P_{outputs}\} \Rightarrow \{Q_{inputs}\}$

Table 6. RTPA process relations (1/2)

triggered by some component, etc. We will also need two special actions. The first one, τ , represents an autonomous change in some part of the system. The second special action, \surd , represents the successful termination. Then, the $RTPA_{core}$ is defined by BNF expression presented in Table 8. Next we describe the operators appearing in the language.

3.1. Stop and exit processes

The stop process (STOP) and exit operator (EXIT) are the inductive basis of the language. The first one represent a *blocked* process, that is, it cannot execute any actions but it has not finished yet. The second one represents a process that has successfully terminated.

3.2. Sequence

The sequence operator¹ ($P \rightarrow Q$) is just the same as the one appearing in the original Table 6 of RTPA process relations: After the successful termination of P , the process Q is executed.

¹We will also call this operator *process prefix*.

No.	Process relation	Syntax	Operational semantics
4.13	Time-driven dispatch	$@t_{hh:mm:ss:ms}$ $\downarrow P_i, i \in \{1, \dots, n\}$	$@t_{1hh:mm:ss:ms} \downarrow P_1$ $ @t_{2hh:mm:ss:ms} \downarrow P_2$... $ @t_{nhh:mm:ss:ms} \downarrow P_n$
4.14	Event-driven dispatch	$@e_{iS} \downarrow P_i, i \in \{1, \dots, n\}$	$@e_{1S} \downarrow P_1$ $ @e_{2S} \downarrow P_2$ $ \dots$ $ @e_{nS} \downarrow P_n$
4.15	Interrupt	$P \parallel \odot (@e_S \nearrow Q \searrow \odot)$	$P \parallel$ system interrupt capture; if $@e_S$ captured = true then (record interrupt point \odot and variables $\downarrow Q$ \rightarrow recover interrupted variables \rightarrow return to the interrupt point \odot and continue P)
4.16	Jump	$P \rightarrow Q$	$P \rightarrow \text{goto } Q \rightarrow Q$

Table 7. RTPA process relations (2/2)

$$\begin{aligned}
P ::= & \text{STOP} \mid \text{EXIT} \mid P \rightarrow Q \mid P_1 + P_2 \mid et \rightarrow P \mid \\
& @t \downarrow P \mid P \parallel \odot (A \nearrow Q \searrow \odot) \mid P \parallel_A Q \mid P \setminus A \mid X \mid X := Q \\
\text{where } & e \in \text{Act} \cup \{\tau\}, t \in \text{Time}, A \subseteq \text{Act} \text{ and } X \in \text{ID}.
\end{aligned}$$

Table 8. RTPA_{core} language

3.3. Choice

This operator is one the most important operators in the RTPA_{core} language. It is used to represent several RTPA relations: Branch, switch, time-driven dispatch, and event-driven dispatch. The behavior of $P + Q$ is the following: Initially, all the behavior of P and Q is available in $P + Q$. However, when an action is performed, $P + Q$ behaves like P or Q depending on the component that performed the action.

Since switch and branch operators in RTPA use the symbol $|$, we might also choose this symbol for the choice operator. In spite of that, we have preferred to choose the symbol $+$ to avoid confusion with the vertical bar that separates the terms in the BNF expression and to avoid confusion with the parallel operator.

Next will show how the RTPA relations mentioned above can be embedded in RTPA_{core} using the choice operator.

3.3.1. Branch

For any boolean expression $expBL$ we consider two events: $expBL_t$ that is enabled when the expression is evaluated to true; and $expBL_f$ that is enabled when it is evaluated to false. Therefore, the RTPA

process relation

$$(?expBL = \text{true}) \rightarrow P \mid (?expBL = \text{false}) \rightarrow Q$$

will be equivalent to the following expression in $RTPA_{core}$

$$(expBL_t \rightarrow P) + (expBL_f \rightarrow Q)$$

3.3.2. Switch

Analogously to the previous case, for any expression $expNUM$ we consider the following events: For all $0 \leq i \leq n$ we have the event $expNUM_i$ that is enabled if expression $expNUM$ is evaluated to i . Hence, the process relation

$$\begin{aligned} ?expNUM = \\ 0 &\rightarrow P_0 \\ 1 &\rightarrow P_1 \\ &\dots \\ n-1 &\rightarrow P_{n-1} \\ n &\rightarrow P_n \\ else &\rightarrow \emptyset \end{aligned}$$

corresponds to the following $RTPA_{core}$ expression

$$(expNUM_0 \rightarrow P_0) + (expNUM_1 \rightarrow P_1) + \dots + (expNUM_{n-1} \rightarrow P_{n-1}) + (expNUM_n \rightarrow P_n)$$

3.3.3. Time-driven dispatch

In the process relation $@t_i \downarrow P_i$ for $1 \leq t_i \leq n$, the i -th process P_i is triggered at the system time t_i . Therefore, it corresponds to the $RTPA$ -core expression

$$@t_1 \downarrow P_1 + @t_2 \downarrow P_2 + \dots + @t_n \downarrow P_n$$

3.3.4. Event-driven dispatch

In this case, the i -th process P_i is triggered by a system event e_i . Thus, the $RTPA_{core}$ expression that represents the behavior of $@e_i \downarrow P_i$ $i \in \{1, \dots, n\}$ is

$$e_1 \rightarrow P_1 + e_2 \rightarrow P_2 + \dots + e_n \rightarrow P_n$$

3.4. Action prefix

The behavior of the *action prefix* $et \rightarrow P$ is simple. First, it can execute the action e at time t and then it behaves like P . Let us not that $e \in Act \cup \{\tau\}$. If e is a *visible action*, that is $e \in Act$, the action has an effect that is observable from the outside. On the contrary, if $e = \tau$ then the effect of the action cannot be directly observable.

3.5. Delay operator

The delay operator is used to add time constraints to the system. The process $@t, P$ delays the execution of P until t time units pass (years, months, days, hours, minutes, seconds, milliseconds).

3.6. Interrupt Operator

The interrupt operator corresponds with the *RTPA* interrupt operator. The process $P \parallel \odot (A \nearrow Q \searrow \odot)$ behaves like P , when an event $e \in A$ is triggered, then P is interrupted and Q is executed. Finally, when Q terminates, P is recovered in the state when the interruption happened.

3.7. Parallel Operator

Our parallel operator $P \parallel_A Q$ represents a parallel environment with the same clock where both parts synchronize in the set of actions A . If they synchronize in the set of all actions ($A = Act$), both process execute the actions at the same time just like indicated in the original *RTPA* semantics for process $P \parallel Q$ (4.9 in Table 6). On the contrary, if $A = \emptyset$ then the actions of P and Q are interleaved, just as indicated in the original *RTPA* semantics for the process relation $P \parallel\parallel Q$ (4.11 in Table 6).

3.8. Hiding operator

The hiding operator $P \setminus A$ hides the actions in the set A . These actions are considered internal. Thus, their effect cannot be seen directly from the outside.

3.9. Process identifier and Recursion

Recursion ($X := Q$) is the only loop like operator that we have defined in $RTPA_{core}$. It is well known that any loop operator can be simulated with recursion and conditionals. Hence, we have preferred to leave only this operator in order to have a simpler syntax. In order to express recursion we need a set of *process identifiers ID*. Each process is defined by an expression that contains process identifiers.

It is important to remark that we will only consider *guarded recursion*, that is, any recursion call must be preceded by a prefix operator (action prefix or process prefix). Although it is difficult to explicit this condition in the BNF expression, it is easy to check if recursion is guarded for a given process. The reason to forbid non guarded recursion is that it would yield to undefined behaviors. Let us consider the following process

$$X := X$$

This process is allowed by the syntax but it is not clear what its behavior should be. Hence, to avoid pointless discussions about the meaning of the above process, we simply consider it a useless process.

Next we will see how the loop operators in *RTPA* relations can be expressed in $RTPA_{core}$ by using recursion.

3.9.1. For-do

Let us suppose that we have the process relation $P = R_{i=1}^n P(i)$. This process relation can be translated to the following set of $RTPA_{core}$ processes

$$\begin{aligned} P_0 &:= \text{EXIT} \\ P_i &:= P(i) \rightarrow P_{i-1}, \quad 1 \leq i \leq n \\ P &:= P_n \end{aligned}$$

3.9.2. Repeat

Now let us consider the process relation $P = R_{\geq 1}^{expBL \neq true} P'$. The process in $RTPA_{core}$ corresponding to the previous process relation is the following

$$P := P' \rightarrow (expBL_f \rightarrow \text{EXIT} + expBL_t \rightarrow P' \rightarrow P)$$

3.9.3. While-do

In this case we have $P = R_{\geq 0}^{expBL \neq true} P'$. That can be encoded as

$$P := expBL_f \rightarrow \text{EXIT} + expBL_t \rightarrow P' \rightarrow P$$

4. Operational Semantics

In order to give a testing semantics following [14], first we have to provide the language with an SOS operational semantics that is presented in Table 9. The rules of the operational semantics need an auxiliary function $\text{upd}(t, P)$, which represents the passing of t time units on P . Formally, it is a function

$$\text{upd}(\cdot, \cdot) : \text{Time} \times RTPA_{core} \mapsto RTPA_{core}$$

that is inductively defined in Table 10. It is important to note that in the definition of this function we have omitted the case of recursion. This is because the recursive calls of the $\text{upd}(\cdot, \cdot)$ function do not propagate to the right term of the prefix operators (action prefix and process prefix). Thus, since we are only considering guarded recursion, it is not necessary to explicit the value of the function in the case of recursion.

Formally, the operational semantics of $RTPA_{core}$ is given by the relation

$$\rightarrow \subseteq RTPA_{core} \times (\text{Act} \cup \{\tau, \sqrt{}\} \times \text{Time}) \times RTPA_{core}$$

defined by the rules in Table 9. First, let us give a brief explanation of the rules.

[EXIT] The only action that EXIT process can execute is successful termination. Successful termination is executed as soon as it is available.

[PRE] This rule specifies that the process $et \rightarrow P$ can execute action e at time t .

[EXIT]	$\text{EXIT} \xrightarrow{\sqrt{0}} \text{STOP}$	
[PRE]	$et \rightarrow P \xrightarrow{et} P, \quad e \in \text{Act} \cup \{\tau\}$	
[SEQ1]	$\frac{P \xrightarrow{et} P', \quad P \xrightarrow{\sqrt{t'}} \not\rightarrow t' < t}{P \rightarrow Q \xrightarrow{et} P' \rightarrow Q}, \quad e \neq \sqrt{\quad}$	
[SEQ2]	$\frac{P \xrightarrow{\sqrt{t}} P', \quad P \xrightarrow{\sqrt{t'}} \not\rightarrow t' < t}{P \rightarrow Q \xrightarrow{it} Q}$	
[CH1]	$\frac{P \xrightarrow{at} P', \quad Q \xrightarrow{it'} \not\rightarrow t' < t}{P + Q \xrightarrow{at} P'}$	$\frac{P \xrightarrow{at} P', \quad Q \xrightarrow{it'} \not\rightarrow t' < t}{Q + P \xrightarrow{at} P'}$
[INT1]	$\frac{P \xrightarrow{et_1} P'}{P \parallel \odot (E \nearrow Q \searrow \odot) \xrightarrow{at_1} P'}, \quad e \notin E$	
[INT2]	$\frac{P \xrightarrow{et_1} P'}{P \parallel \odot (E \nearrow Q \searrow \odot) \xrightarrow{\tau t_1} Q \rightarrow (P' \parallel \odot (E \nearrow Q \searrow \odot))}, \quad e \in E$	
[DEL]	$\frac{P \xrightarrow{et_1} P'}{\text{@}t_1.P \xrightarrow{e(t+t_1)} P'}$	
[PAR]	$\frac{P \xrightarrow{et} P', \quad Q \xrightarrow{it'} \not\rightarrow t' < t}{P \parallel_A Q \xrightarrow{et} P' \parallel_A \text{upd}(t, Q)}, \quad e \notin A$	$\frac{P \xrightarrow{et} P', \quad Q \xrightarrow{it'} \not\rightarrow t' < t}{Q \parallel_A P \xrightarrow{et} \text{upd}(t, Q) \parallel_A P'}, \quad e \notin A$
[SYN]	$\frac{P \xrightarrow{at} P', \quad Q \xrightarrow{at} Q'}{P \parallel_A Q \xrightarrow{at} P' \parallel_A Q'}, \quad a \in A$	
[HD1]	$\frac{P \xrightarrow{et} P', \quad P \xrightarrow{at'} \not\rightarrow t' < t, a \in A}{P \setminus A \xrightarrow{et} P' \setminus A}, \quad e \notin A$	
[HD2]	$\frac{P \xrightarrow{at} P', \quad P \xrightarrow{a't'} \not\rightarrow t' < t, a' \in A}{P \setminus A \xrightarrow{it} P' \setminus A}, \quad a \in A$	
[REC]	$\frac{X := P, P \xrightarrow{et} P'}{X \xrightarrow{et} P'}$	

Table 9. Operational Semantics.

[SEQ1] and [SEQ2] These rules indicate the behavior of the sequence operator. Its behavior is the one indicated by the original *RTPA* semantics defined in Table 6. The process $P \rightarrow Q$ behaves like the first component P until it successfully terminates; then, it behaves like the second component Q .

[CH1] The behavior of $P + Q$ is specified by this rule. Initially, the process $P + Q$ is able to execute the actions that are available to either P or Q . Whenever an action is executed, the process behaves like the process that has executed the action.

[INT1] and [INT2] The interrupt operator behaves as indicated in the original *RTPA* semantics defined in Table 7. The process $P \parallel \odot (A \nearrow Q \searrow \odot)$ initially behaves like P . If a captured event is triggered ($a \in A$) then the execution of P is suspended and Q is executed. When Q successfully terminates, the process P is resumed in the state it was when the event was triggered.

[DEL1] This rule specifies the delay operator. The process $\text{@}t_1.P$ can perform the same actions as the

$\text{upd}(t, P) =$	{	P STOP $e(t_1 - t) \rightarrow P_1$ $\text{upd}(t, P_1) \rightarrow P_2$ $@(t_1 - t) \downarrow P$ $\text{upd}(t - t_1, P)$ $\text{upd}(t, P_1) \text{ op } \text{upd}(t, P_2)$ $\text{upd}(t, P_1) \parallel \odot(A \nearrow P_2 \searrow \odot)$ $\text{upd}(t, P_1) \setminus A$	if $P = \text{STOP}$, $P = \text{DIV}$, or $P = \text{EXIT}$, if $P = et_1 \rightarrow P_1$ and $t_1 < t$ if $P = et_1 \rightarrow P_1$ and $t_1 \geq t$ if $P = P_1 \rightarrow P_2$ if $P = @t_1 \downarrow P$ and $t_1 > t$ if $P = @t_1 \downarrow P$ and $t_1 \leq t$ if $P = P_1 \text{ op } P_2$, $\text{op} \in \{+, \parallel_A\}$ if $P = P_1 \parallel \odot(A \nearrow P_2 \searrow \odot)$ if $P = P_1 \setminus A$
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Table 10. Auxiliary function $\text{upd}(t, P)$.

process P but delayed t time units.

[PAR] and [SYN] These rules specify the behavior of the parallel composition $P \parallel_A Q$. This process interleaves the actions that both processes (P and Q) can execute as long as they do not belong to the set of synchronization events A (Rule **[PAR]**). If the action belongs to A then both processes must execute it synchronously (Rule **[SYN]**).

[HD1] and [HD2] The hiding operator hides the actions belonging to the set A . So, the process $P \setminus A$ behaves like P as long as it does not execute actions from the set A . This operator does not prevent the execution of actions in the set A ; it just consider them internal actions. Internal actions do not need any interaction. So, there is no reason to delay its execution and hence, they are executed as soon as they are available. That is why we have to check that no actions from the set A may be executed before any other action is executed.

[REC] Finally, the recursion rule indicates that a process identifier behaves like the process that defines it.

Before going any further, we must point out that some rules in Table 9 have negative premises. This can be fatal for a SOS transition system because it could lead to contradictory assessments. To prevent this we follow [12], and we provide a *stratification* that guaranties the correctness of our transition system. The needed stratification is

$$f(P \xrightarrow{et} P') = t$$

This function is indeed a stratification since time values in *RTPA*, as indicated in Table 4, are specified in milliseconds [yyyy : MM : dd :]hh : mm : ss : ms.

The following two Lemmas state two important properties of the operational semantics. Both are easily proved by structural induction; the proofs are quite straight forward but cumbersome, so we omit the details. The first property is usually called *urgency*, that is, internal actions are executed as soon as they are available. Nevertheless, internal actions have not greater priority than observable actions to be

executed at the same time. Finally, observable actions to be executed before some internal action are not affected by the existence of such an internal action.

Lemma 4.1. If $P \xrightarrow{it} P'$ then $P \xrightarrow{et'} \not\rightarrow$ for any $e \in Act \cup \{\tau, \surd\}$ and $t' > t$. \square

The second important property is that this operational semantics is *finite branching*, that is, *for a given process P there exists a finite number of transitions that P can execute.*

Lemma 4.2. Let $e \in Act \cup \{\tau, \surd\}$ and $t \in Time$. Then set $\{P' \mid P \xrightarrow{et} P'\}$ is finite. \square

Finally, let us comment that is quite easy to define abnormal behaviors in the defined framework. We are referring to Zeno-like processes: Those that can perform an infinite number of actions in a finite amount of time.

Example 4.1. Let us consider the following $RTPA_{core}$ process

$$DIV := \tau 0 \rightarrow DIV$$

This process has the following computation, all the actions being performed at time 0:

$$DIV \xrightarrow{\tau 0} DIV \xrightarrow{\tau 0} DIV \dots$$

This behavior is obviously impossible so it must be avoided. \square

5. Testing Semantics

Once we have defined a SOS operational semantics for $RTPA_{core}$, we can define a testing semantics. In order to identify the abnormal behavior in Example 4.1, we introduce the *convergence* predicate. Just as a step to define it, we need the *weak* predicate

Definition 5.1. We define the *weak convergence* predicate over $RTPA_{core}$, denoted by $P \Downarrow$, as the least predicate that satisfies:

- $STOP \Downarrow$ $EXIT \Downarrow$ and $a \rightarrow P \Downarrow$, for $a \in Act$.
- If $t > 0$ or $P \Downarrow$ then $@t, P \Downarrow$.
- If $P \Downarrow$ and $Q \Downarrow$ then $P \text{ op } Q \Downarrow$, $op \in \{\parallel_A, +\}$.
- If $P \Downarrow$ then $P \rightarrow Q \Downarrow$, $P \setminus A \Downarrow$ and $X \Downarrow$ (where $X := P$).

We define the *strong convergence* predicate, or simply *convergence*, over $RTPA_{core}$, denoted by $P \Downarrow_t$, as the least predicate that satisfies

$$P \Downarrow_t \quad \text{iff} \quad P \Downarrow \text{ and } \forall P' (P \xrightarrow{\tau 0} P' \text{ implies } P' \Downarrow_t).$$

We say that a process diverges, which is denoted by $P \Uparrow$, if $P \Downarrow$ does not hold. If $t \in Time$, it is useful to define the predicate $P \Downarrow_t$ as

$$P \Downarrow_t \quad \text{iff} \quad P \Downarrow \text{ and } \forall P' \forall t' \leq t (P \xrightarrow{\tau t'} P' \text{ implies } P' \Downarrow_t).$$

\square

$$\begin{aligned}
T ::= & \text{OK} \mid \text{STOP} \mid \text{EXIT} \mid T_1 \rightarrow T_2 \mid T_1 + T_2 \mid et \rightarrow T \mid \\
& @t \downarrow T \mid T_1 \parallel \odot (A \nearrow T_2 \searrow \odot) \mid T_1 \parallel_A T_2 \mid T \setminus A \mid X \mid X := T \\
& \text{where } e \in \text{Act} \cup \{\tau\}, t \in \text{Time}, A \subseteq \text{Act} \text{ and } X \in \text{ID}.
\end{aligned}$$

Table 11. Tests

Let us note that a process only diverges due to an infinite computation of internal actions to be instantaneously executed, that is, at time 0.

Tests are very similar to processes but defined by considering an extended grammar where we add a new process, OK, which expresses that the test has been successfully passed. More precisely, tests are generated by the BNF expression given in Table 11.

Now, with test we will use a notation we have not used so far. If $A = \{e_1 t_1, e_2 t_2, \dots, e_n t_n\}$ is a finite set such that $e_i \in \text{Act} \cup \{\tau\}$, $t_i \in \text{Time}$, and for all $et \in A$ there is a test T_{et} , we denote the test

$$\begin{aligned}
& (e_1 t_1 \rightarrow T_{e_1 t_1}) + (e_2 t_2 \rightarrow T_{e_2 t_2}) + \dots + (e_n t_n \rightarrow T_{e_n t_n}) \\
& \text{by } \sum_{et \in A} et \rightarrow T_{et}
\end{aligned}$$

The operational semantics for tests is defined in the same way as for plain processes, but only including a rule for the test OK²:

$$[\text{OK}] \quad \text{OK} \xrightarrow{\text{OK}} \text{STOP}.$$

Finally, we define the composition of a test and a process as

$$P \mid T = (P \parallel_{\text{Act}} T) \setminus \text{Act}$$

That is, the process and the test must synchronize in all visible actions, being this actions automatically hidden.

Now we can relate processes and tests. We execute a process in parallel with a test and we study the computations that they produce. In particular, we are interested in those computations where the tests can execute the OK action. A computation is successful if the test can execute the OK action.

Definition 5.2. Let P be a $RTPA_{\text{core}}$ process and T be a test. Given a computation of $P \mid T$

$$P \mid T = P_1 \mid T_1 \xrightarrow{\tau t_1} P_2 \mid T_2 \cdots P_k \mid T_k \xrightarrow{\tau t_k} P_{k+1} \mid T_{k+1} \cdots$$

we say that it is

- *Complete* if it is finite and blocked (no further step is allowed) or infinite.

²To be exact, we should extend the definition of the operational semantics of both processes and tests to mixed terms defining their composition, since these mixed terms are neither processes nor tests. Since this extension is immediate we have preferred to avoid this formal definition.

- *Successful* if there exists some k such that $T_k \xrightarrow{\text{OK}}$.

□

Next we present the notion of test passing. Depending if all the computations of the parallel composition of a process and a test are successful, or just some computations are successful; we say that the process *must pass* or *may pass* the test.

Definition 5.3. Let P be a $RTPA_{core}$ process and T be a test.

- We say that P *must pass* the test T , denoted by $P \text{ must } T$, iff all complete computations of $P \mid T$ are successful.
- We say that P *may pass* the test T , denoted by $P \text{ may } T$, iff there exists at least one successful computation of $P \mid T$.

□

Finally, we can define relations between processes. Intuitively, a process P is better than Q if it P passes more tests than Q . Since tests can be passed in *may sense* or in *must sense* we can define two relations:

Definition 5.4. Let P and Q be $RTPA_{core}$ processes.

- We write $P \sqsubseteq_{\text{must}} Q$ iff whenever $P \text{ must } T$ we have also $Q \text{ must } T$.
- We write $P \sqsubseteq_{\text{may}} Q$ iff whenever $P \text{ may } T$ we have also $Q \text{ may } T$.

□

6. Operational characterization

Even though our testing semantics is very intuitive, it is hard to handle. In order to check whether $P \sqsubseteq_{\text{must}} Q$ or $P \sqsubseteq_{\text{may}} Q$, according to Definition 5.4, we should prove the property for all possible tests. Unfortunately, the number of tests is infinite. Therefore, it is necessary to find an alternative way to check the relationship between two processes. This alternative definition will be given directly from the operational semantics defined in Section 4. From that operational semantics, and independently from the tests, we can compute what we call *states* and *barbs* of a process.

6.1. Sets of states

In order to characterize our testing semantics we will consider some kind of sets of timed actions, which we call *states*, that represent any of the possible *local configurations* of a process. In a state we have the set of timed actions offered and we also have the time, if any, at which the process becomes undefined, that is, divergent. In order to capture divergence (or equivalently undefinition), with a simple notation, we introduce a new element $\Omega \notin Act$, that represents an undefined substate. Then, we consider the sets $Act_\Omega = Act \cup \{\Omega\}$, $TAct = Act \times Time$, and $Act_\Omega \times Time = Act_\Omega \times Time$.

Definition 6.1. We say that $A \subseteq Act_\Omega \times Time$ is a *state* if

- At most there is a single element $\Omega t \in A$, that is, $\Omega t, \Omega t' \in A$ implies $t = t'$.
- If $\Omega t \in A$ then t is the maximum time in A , that is, $\Omega t, at' \in A$ implies $t' < t$.

We will denote by \mathcal{ST} the set of states. □

The following example illustrates the formerly introduced concept

Example 6.1. Let us consider the following *RTPA* process

$$\begin{aligned}
 P = & \ @00 : 00 : 00 : 01 \downarrow_a \rightarrow \text{STOP} \\
 & \ @00 : 00 : 00 : 02 \downarrow_\tau \rightarrow \ @00 : 00 : 00 : 01 \downarrow_b \rightarrow \text{STOP} \\
 & \ @00 : 00 : 00 : 01 \downarrow_\tau \rightarrow c \rightarrow \text{STOP} \mid \\
 & \ @00 : 00 : 00 : 01 \downarrow_\tau \rightarrow \text{DIV}
 \end{aligned}$$

This process is represented in $RTPA_{core}$ as the following process

$$P = (\ @1 \downarrow_a 0 \rightarrow \text{STOP}) + \ @2 \downarrow_\tau 0 \rightarrow \left(\begin{array}{l} (\ @1 \downarrow_b 0 \rightarrow \text{STOP}) + \\ \ @1 \downarrow_\tau 0 \rightarrow (c0 \rightarrow \text{STOP} + \ @1 \downarrow_\tau 0 \rightarrow \text{DIV}) \end{array} \right)$$

In this process, if no action is executed and 4 units time pass, the process becomes DIV (see Example 4.1), that is undefined. If the surrounding processes want to synchronize in action a at time 1, this synchronization will take place for sure. The same happens with action c at time 3. But action b at time 3 if offered simultaneously with an internal action. So, if the surrounding processes want to synchronize with b at time 3, this synchronization may take place or not. These ideas can be formally expressed by saying that P has two states: $\{a1, c3, \Omega 4\}$ and $\{a1, b3, c3, \Omega 4\}$.

More generally, when we have a process such that $P \xrightarrow{it} P'$ and A is a state of P' , the state A splits into two states: the state $S(P) \cup (A + t)$, in which actions at time t are included, and another state $(S(P) \upharpoonright t)^3 \cup (A + t)$, in which actions at time t are not. So, actions in $S(P)$ whose time is strictly less than t cannot be rejected, but actions just at time t could be rejected. □

Next we give some auxiliary definitions.

Definition 6.2.

- We define the function $nd(\cdot) : \mathcal{ST} \mapsto Time \cup \{\infty\}$, which give us the time at which a state becomes undefined, (not defined function), by:

$$nd(A) = \begin{cases} t & \text{if } \Omega t \in A \\ \infty & \text{otherwise} \end{cases}$$

- Given a state $A \in \mathcal{ST}$ and a time $t \in Time$, we define:

$$A + t = \{a(t + t') \mid at' \in A\} \quad A \upharpoonright t = \{at' \mid at' \in A \text{ and } t' < t\}$$

- If $A \in \mathcal{ST}$, we define its set of timed actions as $TAct(A) = A \upharpoonright \text{nd}(A)$.
- If $A \subseteq TAct$ and $t \in \text{Time}$, we will say that $A < t$ (resp. $A \leq t$) iff for all $at' \in A$ we have $t' < t$ (resp. $t' \leq t$).

□

Finally, in order to define the set of states of a process, we have to define the partial order \prec between states.

Definition 6.3. Given states A_1 and A_2 , we write $A_1 \prec A_2$ iff $\text{nd}(A_1) \leq \text{nd}(A_2)$ and $TAct(A_1) = A_2 \upharpoonright \text{nd}(A_1)$. □

The above definition can be read as follows: A state A_1 is *less defined* than a state A_2 if the following two conditions hold A_1 becomes undefined earlier than A_2 and before the time in which A_1 becomes undefined, both states include the same timed actions. It is easy to check that the relation \prec is a partial order; moreover it is a complete partial order.

Definition 6.4. Given a nondecreasing chain of states $A_1 \prec A_2 \cdots$, we define

$$\text{lub}(\{A_k \mid k \in \mathbb{N}\}) = \left(\bigcup_{i \in \mathbb{N}} TAct(A_i) \right) \cup \begin{cases} \{\Omega t\} & \text{if } \exists k \forall l \geq k \ \Omega t \in A_l \\ \emptyset & \text{otherwise.} \end{cases}$$

□

It is easy to check that the state $\text{lub}(\{A_k \mid k \in \mathbb{N}\})$ defined above is the least upper bound of the nondecreasing chain $\{A_k \mid k \in \mathbb{N}\}$.

6.2. States of a Process

In order to define the set of states of a process we have first to define the initial set of timed actions that a process P can perform, which is given by

$$S(P) = \{at \mid P \xrightarrow{at}, a \in \text{Act}\}.$$

We will generalize the procedure given in example 6.1. First, we observe that a state includes the actions that a process can execute after a *finite* number of internal actions. Here we find a very important difference with respect to untimed process algebras: A process P can execute an infinite sequence of internal actions without diverging, for instance $P := @1 \downarrow \tau 0 \rightarrow P$. This is why we have first to define the set of states that can be reached after k steps.

Definition 6.5. Let P be a process and $k \in \mathbb{N}$. We define the set $\text{st}(k, P)$ as the least set of states that satisfies:

- If $P \uparrow$ then $\text{st}(k, P) = \{\{\Omega 0\}\}$.
- If $P \downarrow$ then

- If $P \not\stackrel{it}{\rightarrow}$ then $\text{st}(k, P) = \{S(P)\}$.
- If $P \stackrel{it}{\rightarrow} P'$ and $P' \uparrow$, or $k = 1$, then $\text{st}(k, P) = \{(S(P) \upharpoonright t) \cup \{\Omega t\}\}$.
- If $P \stackrel{it}{\rightarrow} P'$, $P \Downarrow_t$, $A \in \text{st}(k-1, P')$ and $k > 1$, then $(S(P) \upharpoonright t) \cup (A+t)$, $S(P) \cup (A+t) \in \text{st}(k, P)$.

We say that $A \in \mathcal{ST}$ is a state of P , denoted by $A \in \mathcal{A}(P)$, iff there exists a sequence $\{A_i \in \text{st}(i, P) \mid i \in \mathbb{N}\}$ such that $A_i \prec A_{i+1}$ and $A = \text{lub}(\{A_i \mid i \in \mathbb{N}\})$. \square

It is clear that the previous definition will be hard to use. So, an alternative characterization is necessary. The following proposition states how a state can be computed from a given computation of internal actions.

Proposition 6.1. Let P be a process. The set $\mathcal{A}(P)$ is the set of states $A \in \mathcal{ST}$ which can be generated as described below from a *complete* computation (what means that it is either infinite, or there exists a final process such that either no internal action is possible or it diverges)

$$P = P_1 \xrightarrow{\tau t_1} P_2 \xrightarrow{\tau t_2} \cdots P_k \xrightarrow{\tau t_k} P_{k+1} \cdots$$

where we denote by P_n the final process if the computation is finite, and taking $t^i = \sum_{j=1}^{i-1} t_j$, we have $P_i \Downarrow_{t^i}$ for all $i < n$.

- For each infinite computation we have $\bigcup_{i \in \mathbb{N}} (A_i + t^i) \in \mathcal{A}(P)$, where for each $i \in \mathbb{N}$ we have either $A_i = S(P_i)$ or $A_i = S(P_i) \upharpoonright t_i$.
- For each finite computation we have $\bigcup_{i=1}^n (A_i + t^i) \in \mathcal{A}(P)$, where for each $i \leq n-1$ we have either $A_i = S(P_i)$ or $A_i = S(P_i) \upharpoonright t_i$; except if $P_n \uparrow$ where A_{n-1} must be equal to $S(P_{n-1}) \upharpoonright t_{n-1}$; $A_n = S(P_n)$ if $P_n \Downarrow$, otherwise $A_n = \{\Omega 0\}$. \square

Proof:

It is a consequence of the finite branching property of the operational semantics. \square

6.3. Barbs

A *barb* is a generalization of an acceptance [14], but additional care must be taken about the actions that the process offers *before* any action has been executed. First we introduce the concept of *b-trace*, which is a generalization of the notion of trace. A b-trace, bs , is a sequence, $A_1 a_1 t_1 A_2 a_2 t_2 \cdots A_n a_n t_n$, that represents the execution of the sequence of timed actions $a_1 t_1 a_2 t_2 \cdots a_n t_n$, and after the execution of $a_1 t_1 \cdots a_{i-1} t_{i-1}$, the timed actions in A_i were offered before accepting $a_i t_i$. Then a barb is a b-trace followed by a state, that represents a configuration of a process after executing a b-trace.

To illustrate this let us recall Example 6.1. Where we have that process P may execute action b at time 3, but action a at time 1 is always possible. So, we have that process P can execute the b-trace $\{a1\}b3$ to become STOP afterwards. Urgency is the reason why we have to keep the information about

the actions the process could have executed *before* any other. In this example, if the environment could synchronize in action a at time 1, the process should do it, and then action b at time 3 would be no longer possible. The full set of barbs of P is the following

$$\text{Barb}(P) = \left\{ \begin{array}{l} \{a1, c3, \Omega4\}, \{a1, b3, c3, \Omega4\}, \\ \emptyset a1\emptyset, \{a1\}b3\emptyset, \{a1\}c3\emptyset \end{array} \right\}$$

The next definition formally introduce these concepts.

Definition 6.6.

- We say that a *b-trace* is a finite sequence, $bs = A_1 a_1 t_1 \cdots A_n a_n t_n$, where $n \geq 0$, $a_i t_i \in TAct$, $A_i \subseteq TAct$, and $A_i < t_i$. We say that $\text{lon}(b) = n$; if $n = 0$ we have the empty b-trace denoted by ϵ .
- A *barb* b is a sequence $b = bs \cdot A$ where bs is a b-trace and A is a state. We will write the barb $\epsilon \cdot A$ as A , so we will consider any state A as a barb. \square

In order to define the barbs of a process we need first the some auxiliary notations.

Definition 6.7. Given $t \in T$, a b-trace $bs = A_1 a_1 t_1 \cdot bs_1$ and a set of timed actions $A \subseteq TAct$ such that $A < t$, we define $(A, t) \sqcup bs = (A \cup (A_1 + t))a(t_1 + t) \cdot bs'$.

Let P, P' be processes and bs be a b-trace. We define the relation $P \xrightarrow{bs} P'$ as follows

- $P \xrightarrow{\epsilon} P$.
- If $P \Downarrow_t, P \xrightarrow{it} P_1$, and $P_1 \xrightarrow{bs'} P'$ with $bs \neq \epsilon$ then $P \xrightarrow{(S(P)\uparrow t, t) \sqcup bs'} P'$.
- If $P \Downarrow_t, P \xrightarrow{at} P_1$, and $P_1 \xrightarrow{bs'} P'$ then $P \xrightarrow{(S(P)\uparrow t)at \cdot bs'} P'$.

Let $b = bs \cdot A$ be a barb and P be a process. We say that b is a barb of P , denoted by $b \in \text{Barb}(P)$, iff there exists a process P' such that $P \xrightarrow{bs} P'$ and $A \in \mathcal{A}(P')$. \square

7. Must Testing Semantics

We will use barbs to characterize the testing semantics, this will be done by defining a pre-order between sets of barbs. In order to define this pre-order, first we need the following order relations.

Definition 7.1.

- We define the relation \ll between b-traces as the least relation that satisfies: 1. $\epsilon \ll \epsilon$, 2. If $bs' \ll bs$ and $A' \subseteq A$ then $A'at \cdot bs' \ll Aat \cdot bs$.
- We define the relation \ll between barbs as the least relation that satisfies:
 1. If bs, bs' are b-traces such that $bs' \ll bs$ and A, A' are states such that $\text{nd}(A') \leq \text{nd}(A)$ and $TAct(A') \subseteq A$, then $bs' \cdot A' \ll bs \cdot A$.

2. If A' is a state, $b = A_1 a_1 t_1 \cdot b'$ is a barb such that $\text{nd}(A') \leq t_1$ and $TAct(A') \subseteq A_1$, and $bs' \ll bs$ then $bs' \cdot A' \ll bs \cdot (A_1 a_1 t_1 \cdot b')$.

Let us note that the symbol \ll is overloaded, it is used to relate barbs, set of barbs, and processes. \square

Intuitively, a b-trace bs is *worse* than another one bs' if the actions that appear in both b-traces are the same, and the intermediate sets A_i that appear in bs are smaller than the ones appearing in bs' . For barbs, we must notice that whenever a process is in an undefined state, that is $t = \text{nd}(A) < \infty$, it cannot pass any test *after* that time.

We extend the preorder \ll to sets of barbs as follows:

$$B' \ll B \quad \text{iff} \quad \forall b \in B \exists b' \in B' : b' \ll b$$

The relation \ll between sets of barbs induces a relation between processes, that is, $P \ll Q$ iff $\text{Barb}(P) \ll \text{Barb}(Q)$. The following example illustrates this.

Example 7.1. Let us consider the $RTPA_{core}$ processes

$$\begin{aligned} P &= (\tau 0 \rightarrow \text{STOP}) + (\tau 0 \rightarrow (a1 \rightarrow \text{STOP} + b2 \rightarrow \text{STOP})) \\ Q &= (\tau 0 \rightarrow \text{STOP}) + (\tau 0 \rightarrow (a1 \rightarrow \text{STOP} + b2 \rightarrow \text{STOP})) + (\tau 0 \rightarrow b2 \rightarrow \text{STOP}) \end{aligned}$$

Their set of barbs are given by

$$\text{Barb}(P) = \left\{ \begin{array}{l} \emptyset, \{a1, b2\}, \\ \emptyset a1 \emptyset, \{a1\} b2 \emptyset \end{array} \right\} \quad \text{Barb}(Q) = \left\{ \begin{array}{l} \emptyset, \{b2\}, \{a1, b2\}, \\ \emptyset a1 \emptyset, \emptyset b2 \emptyset, \{a1\} b2 \emptyset \end{array} \right\}$$

We have $Q \ll P$. But for $b = \emptyset b2 \emptyset \in \text{Barb}(Q)$ there is no barb $b' \in \text{Barb}(P)$ such that $b \ll b'$. So, we have $P \not\ll Q$. In $\text{Barb}(P)$, b at time 2 is *always offered* together with a at time 1. If P can synchronize with a test that offers a at time 1 then b at time 2 is never executed. On the contrary, in Q , b at time 2 may be executed. In fact, if we consider the test

$$T = (a1 \rightarrow \text{OK}) + (\tau 2 \rightarrow \text{OK}) + (b2 \rightarrow \text{STOP})$$

it is easy to check that P must T and Q must T . So, we can conclude $P \not\sqsubseteq_{\text{must}} Q$. \square

The rest of the section is devoted to prove the desired characterization, that is,

$$P \sqsubseteq_{\text{must}} Q \quad \text{iff} \quad P \ll Q$$

First we need the following result.

Lemma 7.1. Let P be a process such that $P \Downarrow$ and $P \xrightarrow{it'} \not\rightarrow$ for $t' < t$. Then we have

- $S(P) = (S(P) \upharpoonright t) \cup (S(\text{upd}(P, t)) + t)$.
- $A \in \mathcal{A}(\text{upd}(P, t))$ iff $(S(P) \upharpoonright t) \cup (A + t) \in \mathcal{A}(P)$.
- If $bs \neq \epsilon$ then $\text{upd}(P, t) \xrightarrow{bs} P'$ iff $P \xrightarrow{(S(P) \upharpoonright t, t) \sqcup bs} P'$. \square

Proof:

By structural induction we have $\text{upd}(P, t) \xrightarrow{et'} P'$ iff $P \xrightarrow{e(t'+t)} P'$, and then the result is immediate. \square

Lemma 7.2. Let P be a process and T be a test. We have

- Let $bs = A_1 a_1 t_1 \cdots A_n a_n t_n$ and $bs' = A'_1 a_1 t_1 \cdots A'_n a_n t_n$ be b-traces such that $A_i \cap A'_i = \emptyset$. If $T \xrightarrow{bs} T'$ and $P \xrightarrow{bs'} P'$ then there exists a computation from $P | T$ to $P' | T'$.
- Let $A \in \mathcal{A}(P)$, and let us suppose that T has a computation $T = T_1 \xrightarrow{it_1} T_2 \xrightarrow{it_2} T_3 \cdots$ such that $A \cap ((S(T_i) \upharpoonright t_i) + t^i) = \emptyset$, where $t^i = \sum_{j=1}^{i-1} t_j$. Then,
 - If $t^i < \text{nd}(A)$ then there exists a process P_i such that there is a computation from $P | T$ to $P_i | T_i$.
 - If $t^i < \text{nd}(A)$ and $t^{i+1} \geq \text{nd}(A)$, then there exists a process P' and a test T' such that $P' \upharpoonright$, $T' = \text{upd}(T_i, t')$ for some $t' \geq 0$, and there exists a computation from $P | T$ to $P' | T'$. \square

Proof:

We have just to use the previous lemma and the following facts:

- If $P \xrightarrow{at} P'$, $T \xrightarrow{at} T'$ and $(S(P) \upharpoonright t) \cap (S(T) \upharpoonright t) = \emptyset$ then $P | T \xrightarrow{at} P' | T'$.
- If $P \xrightarrow{it} P'$, $T \xrightarrow{it'} \not\rightarrow$ with $t' < t$ and $(S(P) \upharpoonright t) \cap S(T) = \emptyset$ then $P | T \xrightarrow{it} P' | \text{upd}(T, t)$
- If $T \xrightarrow{it} T'$, $P \xrightarrow{it'} \not\rightarrow$ with $t' < t$ and $(S(T) \upharpoonright t) \cap S(P) = \emptyset$ then $P | T \xrightarrow{it} \text{upd}(P, t) | T'$

 \square

Now we can already prove the left to right side of the characterization:

Theorem 7.1. If $P \ll Q$ then $P \sqsubseteq_{\text{must}} Q$. \square

Proof:

Let T be a test such that P must T . In order to check that Q must T , let us consider any complete computation of $Q | T$

$$Q | T = Q_0 | T_0 \xrightarrow{\tau t_1} Q_1 | T'_1 \cdots \xrightarrow{\tau t_k} Q'_k | T'_k \cdots$$

This computation may be unzipped into a computation of Q

$$Q = Q_{11} \xrightarrow{it_{11}^Q} \cdots Q_{1m_1-1} \xrightarrow{\tau t_{1m_1-1}^Q} Q_{1m_1} \xrightarrow{a_1 t_{1m_1}^Q} Q_{21} \cdots$$

and a computation of T

$$T = T_{11} \xrightarrow{it_{11}^T} \cdots T_{1n_1-1} \xrightarrow{\tau t_{1n_1-1}^T} T_{1n_1} \xrightarrow{a_1 t_{1n_1}^T} T_{21} \cdots$$

where $t_i = \sum_{j=1}^{m_i} t_{ij}^Q = \sum_{j=1}^{n_i} t_{ij}^T$. From the computations of Q and T we can get a sequence of b-traces of Q and T

$$bs_i^Q = A_1^Q a_1 t_1 \cdots A_i^Q a_i t_i, \quad bs_i^T = A_1^T a_1 t_1 \cdots A_i^T a_i t_i,$$

such that $Q \xrightarrow{bs_i^Q} Q_{(i+1)1}$ and $T \xrightarrow{bs_i^T} T_{(i+1)1}$, where

$$A_j^Q = \bigcup_{k=1}^{m_j} \left((S(Q_{jk}) \upharpoonright t_{jk}^Q) + \sum_{l=1}^{k-1} t_{jl}^Q \right), \quad A_j^T = \bigcup_{k=1}^{n_j} \left((S(T_{jk}) \upharpoonright t_{jk}^T) + \sum_{l=1}^{k-1} t_{jl}^T \right).$$

As the computation of P and T is possible, we have that $A_j^Q \cap A_j^T = \emptyset$. Given the b-trace bs_i^Q , there exists a state A such that $b_i = bs_i^Q \cdot A \in \text{Barb}(Q)$. So, there exists $b = bs \cdot A \in \text{Barb}(P)$ such that $b \ll b_i$. If $\text{lon}(b) < \text{lon}(b_i)$, by applying the previous lemma, we could find an unsuccessful computation of $P \mid T$, against our hypothesis. So, we have $\text{lon}(b) = \text{lon}(b_i)$ and then there exists a process P_{i+1} such that there exists a computation from $P \mid T$ to $P_{i+1} \mid T_{(i+1)1}$.

Let us suppose that the sequence of b-traces is infinite. Then for any $k \in \mathbb{N}$ there exists a process P_k such that there exists a computation from $P \mid T$ to $P_k \mid T_{k1}$. As the operational semantics is finite branching, by applying Koning's lemma, we have that there exists an infinite computation in which all the T_{ij} 's appears. Since P must T we have that there exists T_{ij} such that $T_{ij} \xrightarrow{\text{OK}}$. So, the computation of $Q \mid T$ is successful.

Now, let us suppose that there exists a *last* b-trace bs_k^Q . Then, there exists a state A^Q such that

$$A^Q \upharpoonright t^{Qj} = \bigcup_{i=1}^{j-1} \left((S(Q_{(k+1)i}) \upharpoonright t_{(k+1)i}^Q) + \sum_{l=1}^{i-1} t^{Qi} \right) \quad \text{where } t^{Qi} = \sum_{l=1}^{i-1} t_{(k+1)l}^Q$$

and $b^Q = bs_k^Q \cdot A^Q \in \text{Barb}(Q)$. As $P \ll Q$, there exists some $b^P = bs^P \cdot A^P \in \text{Barb}(P)$ such that $b^P \ll b^Q$. Let us suppose that

$$P \xrightarrow{bs^P} P' \quad \text{and} \quad A^P \in \mathcal{A}(P')$$

If $\text{lon}(b^P) < \text{lon}(b^Q)$, by applying the previous lemma we can find an unsuccessful computation of $P \mid T$, against our hypothesis. So we must have $\text{lon}(b^P) = \text{lon}(b^Q)$. Let us consider

$$t^{T,i} = \sum_{j=1}^{i-1} t_{(k+1)j}^T$$

then, for any $t^{T,i} < \text{nd}(A^P)$, there exists a computation from $P' \mid T_{(k+1)1}$ to $P_i \mid T_{(k+1)i}$ for some P_i . If either $\text{nd}(A^P) < \infty$ or the collection of tests is finite, as P must T there must be some test T_{ij} such that $T_{ij} \xrightarrow{\text{OK}}$; so we have that the computation of $Q \mid T$ is successful. If $\text{nd}(A^P) = \infty$ and the collection of tests is infinite, as the operational semantics is finite branching, by applying K\"{i}nning's lemma, we have that there must be some infinite computation, in which all the T_{ij} 's appear. As P must T we have that there exists T_{ij} such that $T_{ij} \xrightarrow{\text{OK}}$, so the computation of $Q \mid T$ is successful. \square

Now we have to prove that $P \sqsubseteq_{\text{must}} Q$ implies $P \ll Q$. First, let us assume that $P \not\ll Q$. Then, by definition, there exists some $b \in \text{Barb}(Q)$ such that there does not exist any $b' \in \text{Barb}(P)$ such that $b' \ll b$. In order to find a test T such that P must T and Q $\not\text{must} T$, we generalize the procedure described in example 7.1. First, we need some auxiliary definitions.

Definition 7.2. Let B be a set of barbs and bs a b-trace. We define the barbs of B after bs as:

$$\text{Barb}(B, bs) = \{b \mid bs \cdot b \in B\}$$

Given a barb b and a set of barbs B such that there does not exist a barb $b' \in B$ with $b' \ll b$, we say that a test T is *well formed with respect to B and b* when it can be derived by applying the following rules:

- If $b = A$ we take a finite set $A_1 \subseteq TAct$ such that for all $A' \in B$ with $\text{nd}(A') \leq \text{nd}(A)$, we have $A_1 \cap A' \neq \emptyset$. Then, by considering

$$T_1 = \begin{cases} it \rightarrow \text{OK} & \text{if } \text{nd}(A) = t < \infty \\ \text{STOP} & \text{otherwise} \end{cases} \quad \text{and} \quad T_2 = \begin{cases} \sum_{at \in A_1} at \rightarrow \text{OK} & \text{if } A_1 \neq \emptyset \\ \text{STOP} & \text{otherwise} \end{cases}$$

we have that $T = T_1 + T_2$ is *well formed with respect to B and b* .

- If $b = Aat \cdot b_1$ we consider any finite set $A_1 \subseteq TAct$ such that $A_1 \cap A = \emptyset$ and any barb $A'at \cdot b'_1 \in B$ satisfying either $A' \cap A \neq \emptyset$ or $b'_1 \ll b_1$. Then, we consider the test

$$T_2 = \begin{cases} \sum_{at \in A_1 \setminus A} at \rightarrow \text{OK} & \text{if } A_1 \setminus A \neq \emptyset \\ \text{STOP} & \text{otherwise} \end{cases}$$

and the set of barbs $B_1 = \{b' \mid A' \subseteq A \text{ and } A'at \cdot b' \in B\}$. If $B_1 \neq \emptyset$ then we can take as T_1 any well formed test with respect to B_1 and b_1 ; otherwise let $T_1 = \text{STOP}$. Then $T = T_1 + T_2 + it \rightarrow \text{OK}$ is a *well formed test with respect to B and b* . \square

It is possible that for a given set of barbs B and a barb b , there does not exist a well formed test T with respect to B and b , because it might not exist the finite set A_1 required in the first part of the definition. But, as the operational semantics is finite branching, for any $B = \text{Barb}(P)$ and $b \in \text{Barb}(Q)$ such that there is no $b' \in B$ with $b' \ll b$, there exists a well formed test T with respect to B and b .

Proposition 7.1. Let T be a well formed test with respect to a set of barbs B and a barb b . Then,

- If $b \in \text{Barb}(Q)$ then Q $\not\text{must} T$.
- If $B = \text{Barb}(P)$ and there is no $b' \in B$ such that $b' \ll b$, then P must T . \square

As a corollary of the previous result we have that the right to left side of the characterization holds

Theorem 7.2. Let P and Q be processes. If $P \sqsubseteq_{\text{must}} Q$ then $P \ll Q$. \square

Proof:

Let us suppose that $P \not\ll Q$. Then, there exists a barb $b \in \text{Barb}(Q)$ such that there does not exist $b' \in \text{Barb}(P)$ verifying that $b' \ll b$. So, we can find a well formed test T with respect to $\text{Barb}(P)$ and b . Then, by the previous proposition we have that $P \text{ must } T$ and $Q \not\text{ must } T$. So, $P \not\sqsubseteq_{\text{must}} Q$, that contradicts our hypothesis. \square

8. May testing semantics

In an untimed framework *may* semantics is simply equivalent to just trace semantics [14]. But when time is introduced the *may* semantics also becomes more complex, since, as usual, we are assuming that internal actions are executed as soon they are available. For that reason it is possible to detect by means of tests not only the actions that have been executed, but also those that could have been chosen instead. To be exact, we will prove that for non-divergent processes the *may* testing semantics is equivalent to the *must* testing semantics. This is so because for any adequate test we can define a dual one in such a way that a process passes the original test in the *must* sense if and only if it does not pass the dual one in the *may* sense.

The situation is much more complicated in the case of non-divergent processes. It is well known that in the untimed case by using *may* testing we can (partially) know the possible behaviors of a process after the instant at which it diverges, what is not possible under *must* semantics. This is also the case in the timed case. In fact, we will present a couple of examples showing that in the general case *may* and *must* testing orderings are incomparable. Hence, another characterization *must* be given for the *may* testing semantics in the general case including divergent processes.

8.1. Divergence free processes

First, we will study the relationship between *may* and *must* testing semantics in the case when the involved processes are divergence free. We will show that in this case, rather surprisingly, both relations are symmetric, that is, we have

$$P \sqsubseteq_{\text{must}} Q \quad \text{iff} \quad Q \sqsubseteq_{\text{may}} P$$

Even more, we will show that *may* testing can be viewed as the *dual* relation of *must* testing in the sense that for any *standard test* T characterizing *must* semantics we can define its *dual* T^* in a very simple and natural way. We will show that a process passes a family of tests in the *must* sense iff it does not pass the corresponding *dual tests* in the *may* sense.

Let us recall that a process P is *divergent*, $P \uparrow$, if P can execute in a row infinitely many internal actions, all of them at (local) time 0. We say that P is *divergence free* when no execution of P leads to a divergent process. For instance, the process $P = a1 \rightarrow \text{STOP}$ is divergence free, while $Q = a1 \rightarrow \text{DIV}$ is not, because of the existence of the computation $Q \xrightarrow{a1} \text{DIV}$. We could formally define divergence freedom directly from the operational semantics, but since this definition is a little cumbersome, we present instead the equivalent characterization in terms of barbs.

Definition 8.1. We say that a process P is *divergence free* iff for each barb $bs \cdot A \in \text{Barb}(P)$ we have $\text{nd}(A) = \infty$. \square

First, we will prove the left to right implication: $P \sqsubseteq_{\text{must}} Q$ implies $Q \sqsubseteq_{\text{may}} P$. We will use the following characterization of the *must* testing order from the previous section: $P \sqsubseteq_{\text{must}} Q$ iff $\text{Barb}(P) \ll \text{Barb}(Q)$. So it is enough the following result.

Proposition 8.1. Let P and Q be processes. We have $\text{Barb}(Q) \ll \text{Barb}(P)$ implies $P \sqsubseteq_{\text{may}} Q$ \square

Proof:

Let T be a test such that P may T . Then, there exist some process P' and some test T' such that $T' \xrightarrow{\text{OK}}$ and there exists a computation from $P \mid T$ to $P' \mid T'$. So, there exist P'' and T'' and a couple of b-traces $bs = A_1 a_1 t_1 \cdots A_n a_n t_n$ and $bs' = A'_1 a_1 t_1 \cdots A'_n a_n t_n$ such that $P \xrightarrow{bs} P''$, $T \xrightarrow{bs'} T''$, $A_i \cap A'_i = \emptyset$, and there exists a computation from $P \mid T$ to $P'' \mid T''$ and another one from $P'' \mid T''$ to $P' \mid T'$.

Now, from the computations of P'' and T'' to P' and T' we get two states $A \in \mathcal{A}(P'')$ and $A' \in \mathcal{A}(T'')$ satisfying $A \cap A' = \emptyset$.

Since $\text{Barb}(Q) \ll \text{Barb}(P)$ and P and Q are divergence free, there exist some barb $b'' = bs'' \cdot A'' \in \text{Barb}(Q)$ with $bs'' = A''_1 a_1 t_1 \cdots A''_n a_n t_n$ and some processes Q' and Q'' such that $A''_i \subseteq A_i$, $A'' \subseteq A$, $Q \xrightarrow{bs''} Q''$ and there is a computation from Q'' to Q' , that generates the state A'' by applying Proposition 6.1. So we have $A''_i \cap A'_i = \emptyset$ and $A'' \cap A' = \emptyset$, so we have a computation from $Q \mid T$ to $Q'' \mid T''$. Since Q is divergence free, we also obtain a computation from $Q'' \mid T''$ to $Q' \mid T'$. \square

Next we prove the right to left side implication. For it, we will adapt the family of *standard tests* characterizing *must* semantics from the previous section. That is, in order to prove $P \not\sqsubseteq_{\text{must}} Q$ we can find a test T such that P must T but Q *not* must T . These tests are similar, but not exactly the same, to those presented in the previous section. Although in order to relate \sqsubseteq_{may} and $\sqsubseteq_{\text{must}}$ we could also use here those tests, we have preferred to use instead this new definition, because by considering the corresponding *dual tests* we can directly obtain that relationship, thus emphasizing the duality between *must* and *may* passing of tests. The tests that constitute the new family of *standard tests* are those obtained by applying the following definition.

Definition 8.2. Given a barb b and a set of barbs B with there is no barb $b' \in B$ such that $b' \ll b$, we say that a test T is *may well formed with respect to B and b* when it can be derived by applying the following rules:

- If $b = A$ then we take any finite set $A_1 \subseteq \text{Act} \times \mathcal{T}$ such that for all $A' \in B$ we have $A_1 \cap A' \neq \emptyset$. Then, by considering

$$T_2 = \left(\sum_{at \in A_1} at \rightarrow \text{OK} \right) + \tau t \rightarrow \text{STOP}, \quad \text{where } t > \max\{t' \mid at' \in A_1\}$$

we have that $T = T_1 + T_2$ is *may well formed with respect to B and b* .

- If $b = Aat \cdot b_1$ then we consider any finite set $A_1 \subseteq \text{Act} \times \mathcal{T}$ with $A_1 \cap A = \emptyset$ and such that all barb $A'at \cdot b'_1 \in B$ either satisfies $A' \cap A_1 \neq \emptyset$ or $b'_1 \ll b_1$. When $A_1 \neq \emptyset$, we consider the test

$$T_2 = \sum_{at \in A_1 \setminus A} at \rightarrow \text{OK}.$$

Besides, by taking the set of barbs $B_1 = \{b' \mid A' \subseteq A \text{ and } A'at \cdot b' \in B\}$, when $B_1 \neq \emptyset$, we can take as T_1 any well formed test with respect to B_1 and b_1 . Then we have that

$$T = \begin{cases} T_1 + T_2 + it \rightarrow \text{OK} & \text{if } A_1 \neq \emptyset, B_1 \neq \emptyset \\ T_1 & \text{if } A_1 \neq \emptyset, B_1 = \emptyset \\ T_2 + it \rightarrow \text{OK} & \text{if } A_1 = \emptyset, B_1 \neq \emptyset \end{cases}$$

is a *may well formed test with respect to B and b* . □

Given an arbitrary set of barbs B and a barb b , it is possible that there does not exist may well formed test T with respect to B and b because the finite set A_1 required in the first part of the definition might not exist. But, as the operational semantics is finitely branching, for all $B = \text{Barb}(P)$ and $b \in \text{Barb}(Q)$ such that there is no $b' \in B$ with $b' \ll b$, there is some well formed test T with respect to B and b . Finally, dual tests are defined as expected.

Definition 8.3. Let T be a test. We define its dual test, denoted by T^* , by interchanging the tailing occurrences of STOP and OK in T . □

The well formed tests and their duals satisfy the following:

Proposition 8.2. Let B be a set of barbs and b be a barb such that and there is no $b' \in B$ with $b' \ll b$. Let us consider a *may well formed test with respect to B and b* T . Then,

$$\begin{aligned} b \in \text{Barb}(Q) & \text{ implies } Q \not\text{must} T \text{ and } Q \text{ may } T^*. \\ B = \text{Barb}(P) & \text{ implies } P \text{ must } T \text{ and } P \not\text{may} T^*. \end{aligned}$$

□

Proposition 8.3. Let P and Q be processes. We have $P \sqsubseteq_{\text{may}} Q$ implies $Q \sqsubseteq_{\text{must}} P$. □

Proof:

Let us suppose that $Q \not\sqsubseteq_{\text{must}} P$. Then, there must exist some $b \in \text{Barb}(P)$ such that there is no $b' \in \text{Barb}(Q)$ verifying $b' \ll b$. Then we take a well formed test T with respect to $\text{Barb}(Q)$ and b such that $Q \text{ must } T$ and $P \not\text{must } T$. Then, by applying the previous proposition, we have $P \text{ may } T^*$ and $Q \not\text{may} T^*$. This contradicts our hypothesis, $P \sqsubseteq_{\text{may}} Q$. □

Finally, as a consequence of Propositions 8.1 and 8.3, we obtain the *may testing characterization theorem* for divergence-free processes that we were looking for:

Theorem 8.1. Let P and Q be processes. We have $P \sqsubseteq_{\text{may}} Q$ iff $Q \sqsubseteq_{\text{must}} P$ □

8.2. Processes with divergences

Once we have studied the relation \sqsubseteq_{may} for non divergent processes we proceed to study it in the general case. Since in the untimed case we already had $\simeq_{\text{must}} \neq \simeq_{\text{may}}$ when divergences appear, we could expect that in the timed setting we would have the same result. This is the case, as the following example shows.

Example 8.1. Let us consider the processes $P = \text{DIV}$ and $Q = \tau 0 \rightarrow \text{DIV} + \tau 0 \rightarrow a1 \rightarrow \text{STOP}$. It is straight forward to show that P and Q are equivalent under *must* testing, that is, $P \sqsubseteq_{\text{must}} Q$ and $Q \sqsubseteq_{\text{must}} P$. On the other hand, we have $Q \not\sqsubseteq_{\text{may}} P$.

We also have $\simeq_{\text{must}} \not\sqsubseteq_{\text{may}}$, what means that we really need divergence freedom in order to prove Proposition 8.3. To show it, let us consider the following processes:

$$\begin{aligned} P &= \tau 0 \rightarrow ((a0 \rightarrow \text{STOP}) + (\tau 2 \rightarrow \text{DIV})) + \tau 0 \rightarrow ((a0 \rightarrow \text{STOP}) + (b0 \rightarrow \text{STOP}) + (\tau 1 \rightarrow \text{DIV})) \\ Q &= \tau 0 \rightarrow ((a0 \rightarrow \text{STOP}) + (\tau 2 \rightarrow \text{DIV})) + \tau 0 \rightarrow ((a0 \rightarrow \text{STOP}) + (b0 \rightarrow \text{STOP}) + (\tau 2 \rightarrow \text{DIV})) \end{aligned}$$

It is not difficult to show, by using Theorem 8.2, that under *may* testing semantics both processes are equivalent, that is, $P \simeq_{\text{may}} Q$. However, we have $Q \not\sqsubseteq_{\text{must}} P$, and so $P \not\sqsubseteq_{\text{must}} Q$. \square

In order to characterize \sqsubseteq_{may} in the general case, we introduce the following notation.

Definition 8.4. Let $bs \cdot A$ and $bs' \cdot A'$ be barbs. We write $bs' \cdot A' \ll_{\text{may}} bs \cdot A$ if $bs' \ll bs$, $\text{nd}(A') \geq \text{nd}(A)$, and $A' \upharpoonright \text{nd}(A) \subseteq A$. We define the relation \ll_{may} between sets of barbs, by writing $B_1 \ll_{\text{may}} B_2$ iff for all $b_1 \in B_1$ there exists $b_2 \in B_2$ such that $b_2 \ll_{\text{may}} b_1$. \square

The rest of the section is devoted to prove the *May Characterization Theorem* for the general case: $P \sqsubseteq_{\text{may}} Q$ iff $\text{Barb}(P) \ll_{\text{may}} \text{Barb}(Q)$. It is an immediate consequence of Propositions 8.4 and 8.5 below. In the following, $\text{t}_{\text{bs}}(bs)$ stands for the *duration* of bs , defined by taking $\text{t}_{\text{bs}}(\epsilon) = 0$ and $\text{t}_{\text{bs}}(A_1 a_1 t_1 \cdot bs_1) = t_1 + \text{t}_{\text{bs}}(bs_1)$. To prove the left to right implication of the theorem, we need a special kind of tests.

Definition 8.5. Let b be a barb. We inductively define the test $T(b)$ as follows:

$$\begin{aligned} T(\epsilon \cdot A) &= \tau t \rightarrow \text{OK} + \sum_{a't' \notin A, t' < t} a't' \rightarrow \text{STOP} && (t = \text{nd}(A)) \\ T(A'a't' \cdot b, t) &= a't' \rightarrow T(b, a, t) + \sum_{a''t'' \notin A', t'' < t} a''t'' \rightarrow \text{STOP} && \square \end{aligned}$$

For these distinguished tests it is easy to check the following property.

Lemma 8.1. Let P and Q be processes. P may $T(b)$ iff there exists $b' \in \text{Barb}(P)$ such that $b' \ll_{\text{may}} b$. \square

Proposition 8.4. Let P and Q be processes. $P \sqsubseteq_{\text{may}} Q$ implies $\text{Barb}(P) \ll_{\text{may}} \text{Barb}(Q)$. \square

Proof:

Let us consider $b = bs \cdot A \in \text{Barb}(P)$. Then, we have P may $T(b)$. Since $P \sqsubseteq_{\text{may}} Q$ we also have Q may $T(b)$, and by applying the previous lemma we get the desired result. \square

Proposition 8.5. Let P and Q be processes. $\text{Barb}(P) \ll_{\text{may}} \text{Barb}(Q)$ implies $P \sqsubseteq_{\text{may}} Q$. \square

Proof:

The proof of this proposition is very similar to that of Proposition 8.1. The only difference is the way that the adequate barb $b'' = bs'' \cdot A'' \in \text{Barb}(Q)$ is found. To do it in this case we have to take into account that $P \ll_{\text{may}} Q$. From this fact, we can also conclude that this barb verifies $bs'' \ll bs$, $A'' \upharpoonright \text{nd}(A) \subseteq A$, and $\text{nd}(A'') \geq \text{nd}(A)$. Then, for the corresponding processes Q' and Q'' we obtain a successful computation from $Q \mid T$ to $Q' \mid T'$. \square

Theorem 8.2. Let P and Q be processes. $P \sqsubseteq_{\text{may}} Q$ iff $\text{Barb}(P) \ll_{\text{may}} \text{Barb}(Q)$. \square

9. Conclusions and Future Work

In this paper we have presented a suitable testing semantics for the process algebra *RTPA*. The syntax of this algebra is very rich and expressive. On the one hand, that expressiveness makes this algebra the ideal framework for a real time system programmer. On the other hand, it makes the algebra quite difficult to study from a formal point of view. We have overcome this problem by defining a core language that is simpler. However, this simpler language is powerful enough to represent to *RTPA* process relations.

Testing semantics for process algebras are very intuitive: two process are equivalent if they present the same behavior when they interact with the environment. The definition of these semantics usually is rather direct. The real problem resides in applying that definition: it requires to take an infinite amount of tests to compare two processes. So, it is usually necessary to provide an alternative characterization to the original definition. That is what we have done in this paper for both the *must* testing semantics and the *may* testing semantics.

Another interesting result is the relationship between the *must* testing semantics and the *may* testing semantics. It turns out that, for a broad class of processes, both relations are *dual* one each other. This class of processes is the *divergence free processes*, that is, the class of processes that do not present abnormal behavior.

As future work, following [14], the characterization of the testing semantics could be used to provide a denotational semantics to *RTPA*. That denotational semantics would be correct and complete with respect the testing semantics presented here. It could be interesting to compare the denotational semantics obtained in this way with the one that has already been defined for *RTPA* in [35].

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